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10/631,886	07/11/2003	2841	827	254-094-CIP-5	7	35	7

J. Scott Denko
 Andrews & Kurth, L.L.P.
 Suite 1700
 111 Congress Ave.
 Austin, TX 78701

CONFIRMATION NO. 1819
CORRECTED FILING RECEIPT



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Date Mailed: 10/06/2004

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Applicant(s)

James W. Cady, Austin, TX;
 Julian Partridge, Austin, TX;
 James Douglas Wehrly JR., Austin, TX;
 James Wilder, Austin, TX;
 David L. Roper, Austin, TX;
 Jeff Buchle, Austin, TX;

Assignment For Published Patent Application

Staktek Group, L.P.

Power of Attorney:

J. Scott Denko-37606

Domestic Priority data as claimed by applicant

This application is a CIP of 10/453,398 06/03/2003
 which is a CIP of 10/005,581 10/26/2001 PAT 6,576,992
 This application 10/631,886
 is a CIP of 10/457,608 06/09/2003
 which is a CIP of 10/005,581 10/26/2001 PAT 6,576,992

Foreign Applications

If Required, Foreign Filing License Granted: 12/11/2003

**The number of your priority application, to be used for filing abroad under the Paris Convention is,
US10/631,886**

Projected Publication Date: Not Applicable

Non-Publication Request: No

Early Publication Request: No

**** SMALL ENTITY ****

Title

Low profile chip scale stacking system and method

Preliminary Class

361

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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